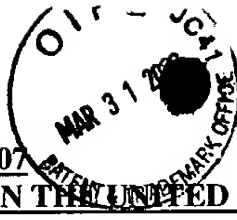


S/N 09/945507



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al.

Examiner: Son Dinh

Serial No.: 09/945507

Group Art Unit: 2824

Filed: August 30, 2001

Docket: 1303.014US1

Title: FLASH MEMORY WITH LOW TUNNEL BARRIER INTERPOLY
INSULATORS

*4/Electrin
w/Amatt. a
Meresa
4-4-03*

AMENDMENT AND RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
Washington, D.C. 20231

This response is to the Restriction Requirement mailed February 25, 2003. Applicant notes that a claim numbering error propagated through many of the claimed dependencies in the pending claim set. Applicant has amended claims 10-16, 18-23, 25-30, 32-41, 43-52, 54-58, 60-69, 71-76 and 78-80 to correct the error.